

FIG. 1

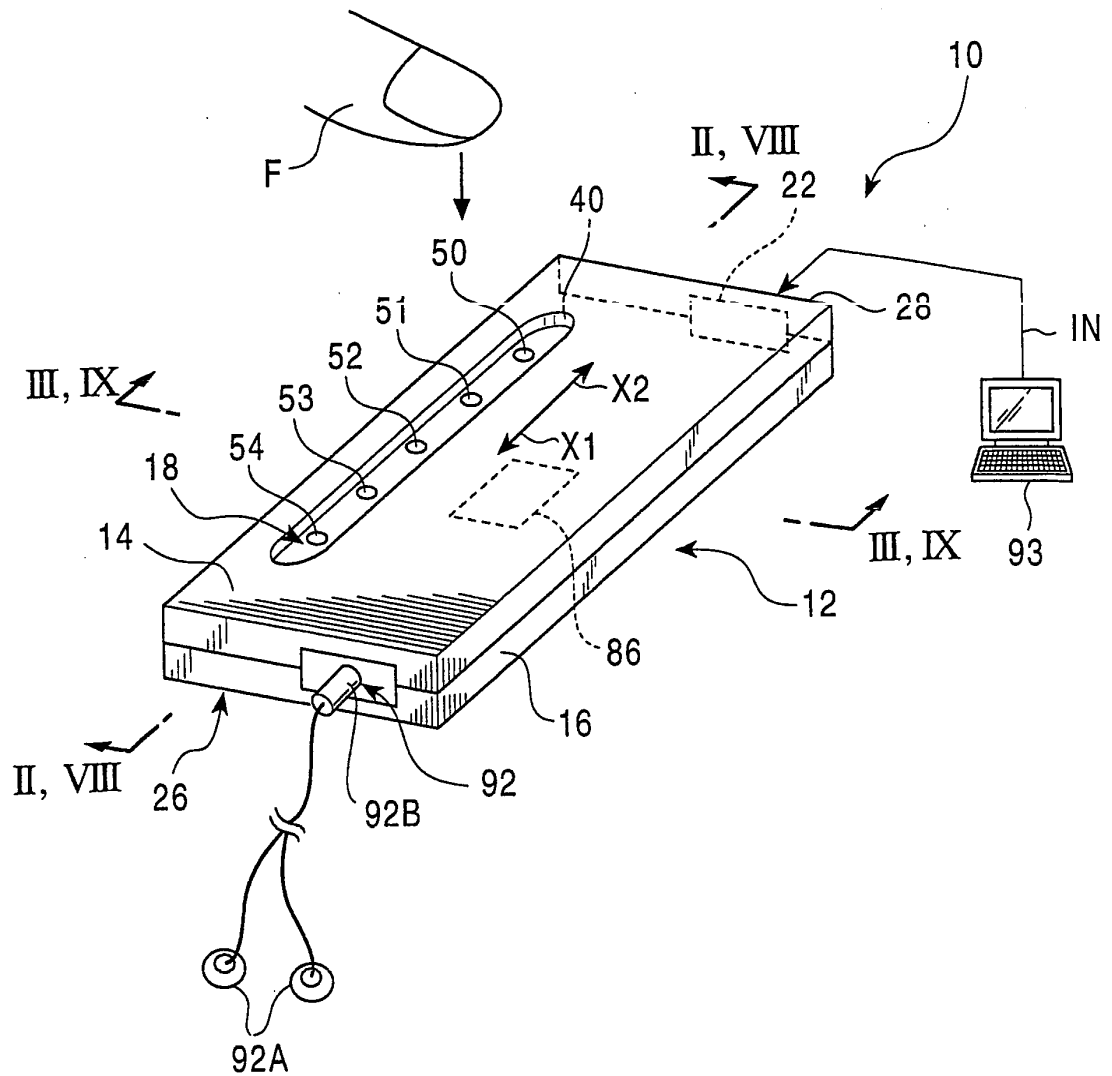


FIG. 2

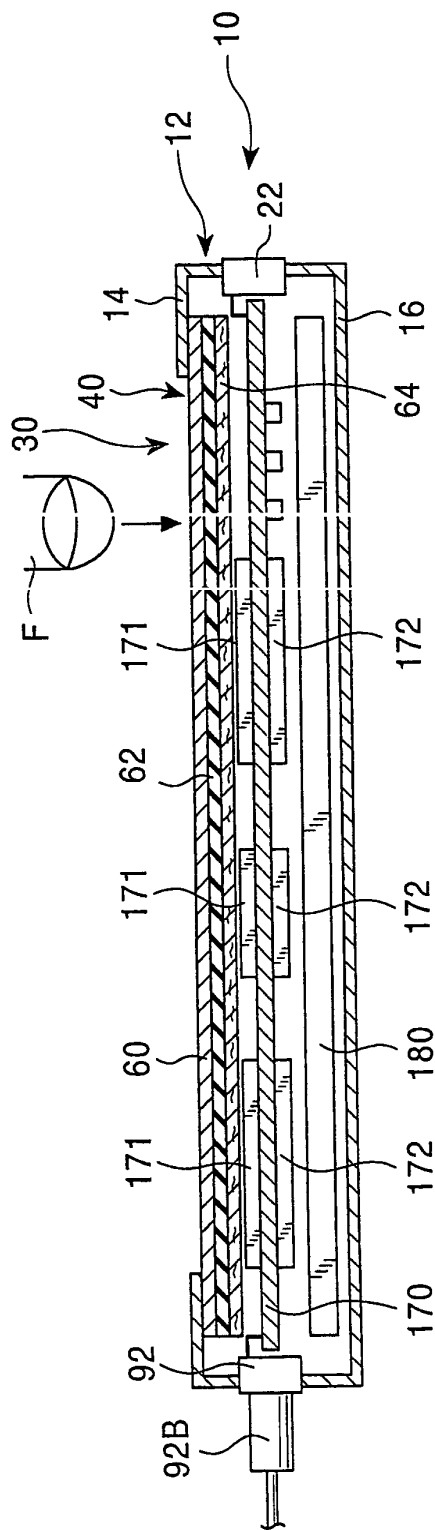


FIG. 3

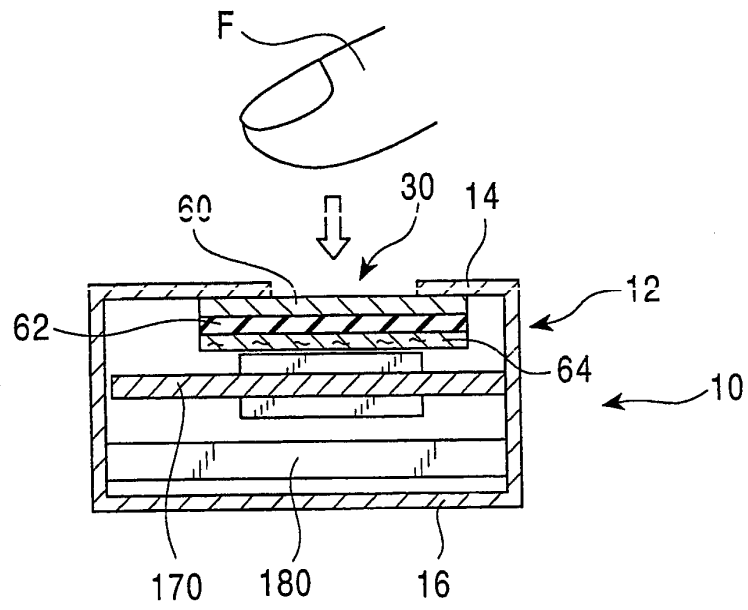


FIG. 4

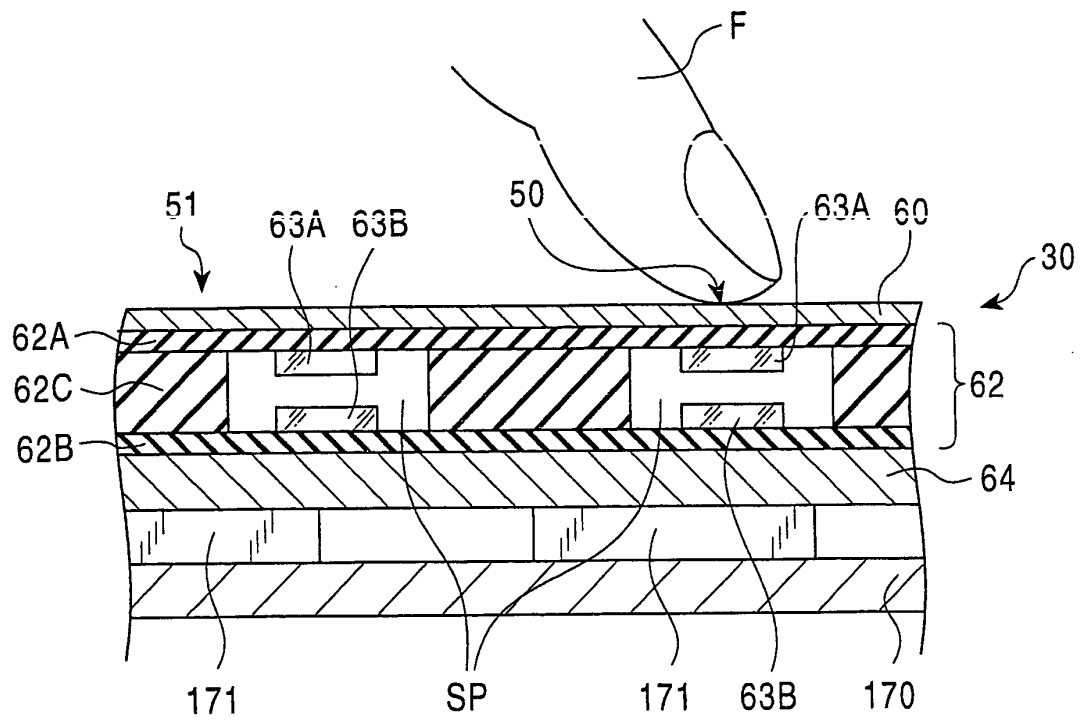


FIG. 5

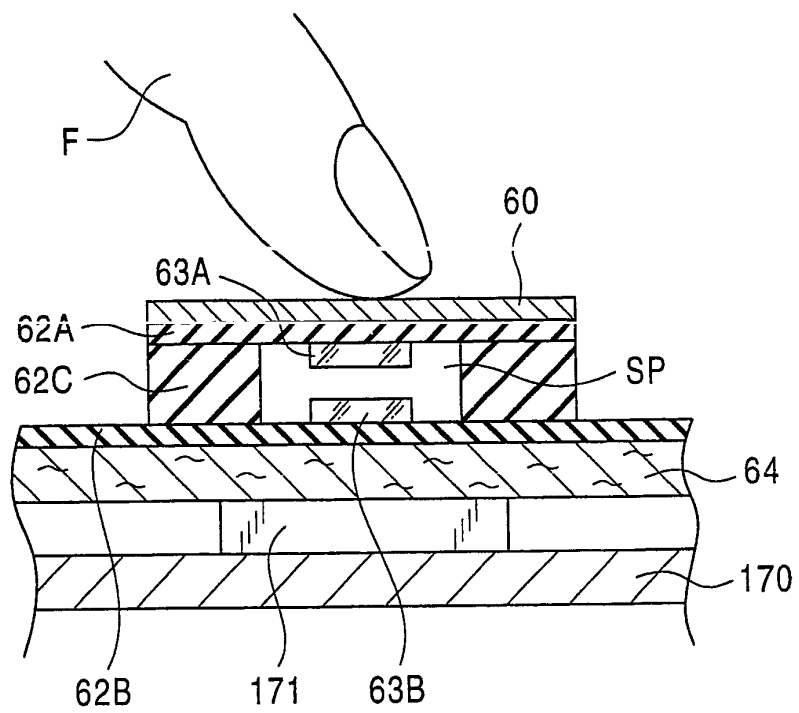


FIG. 6A

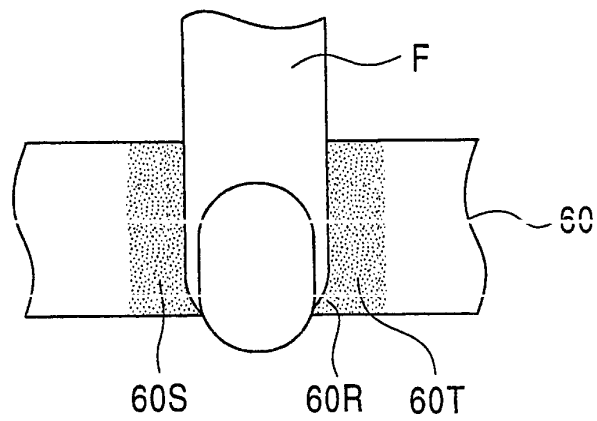


FIG. 6B

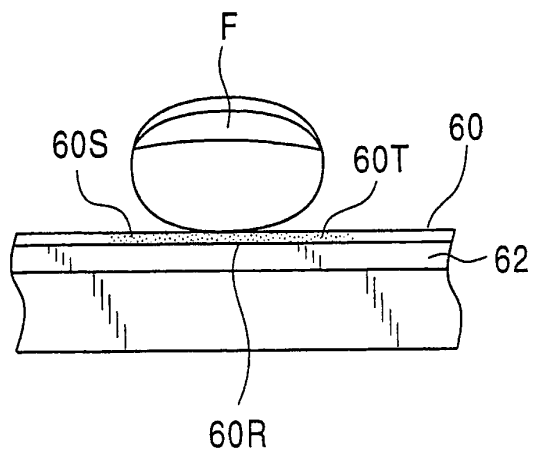


FIG. 7A

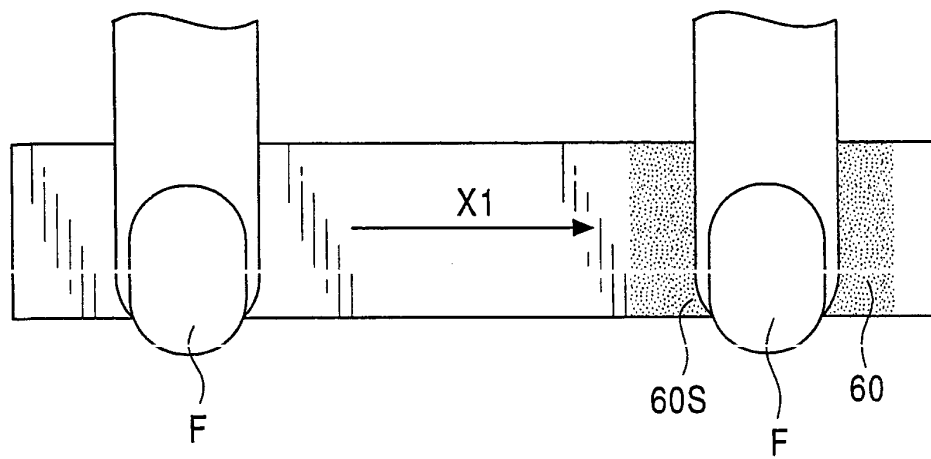


FIG. 7B

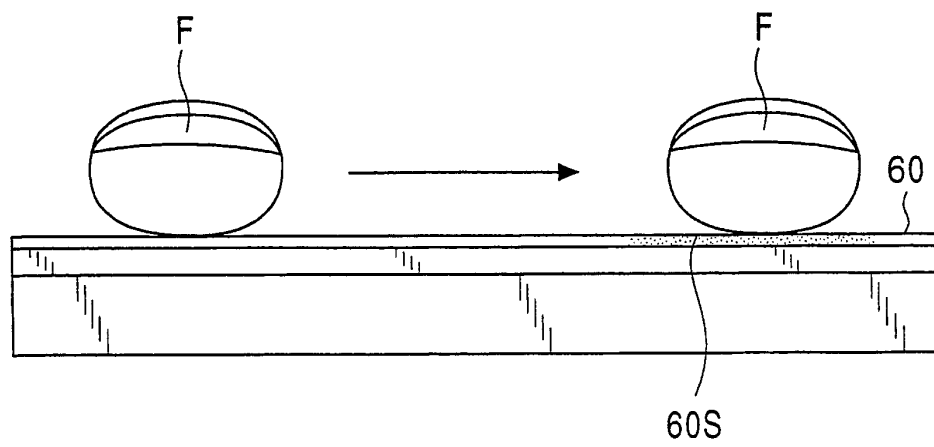


FIG. 8

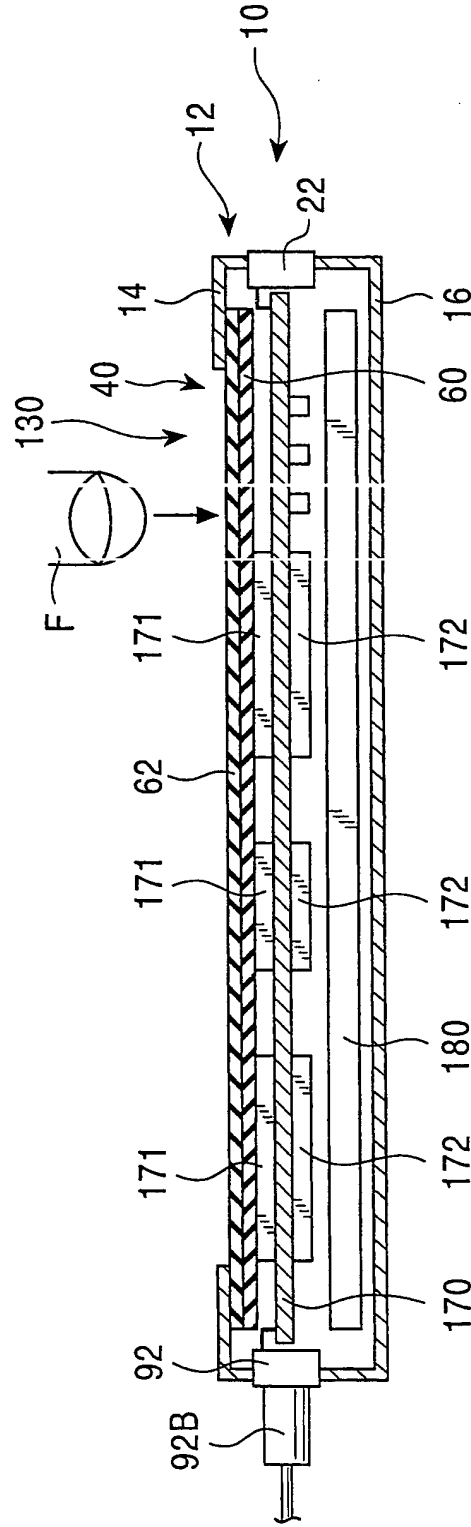




FIG. 9

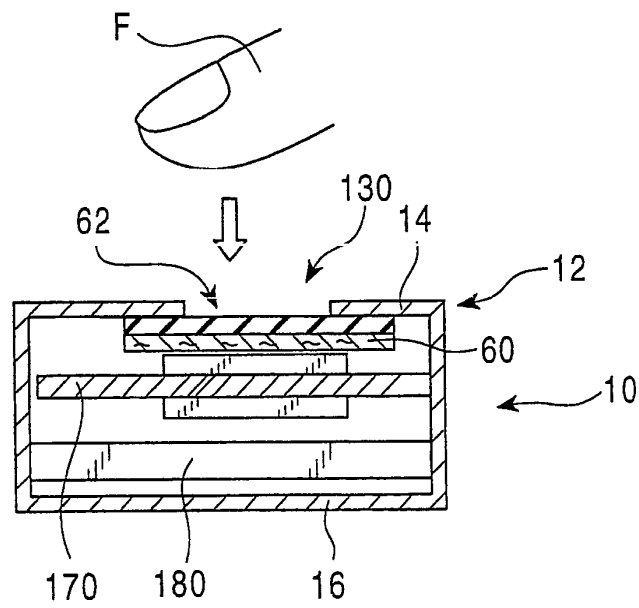


FIG. 10

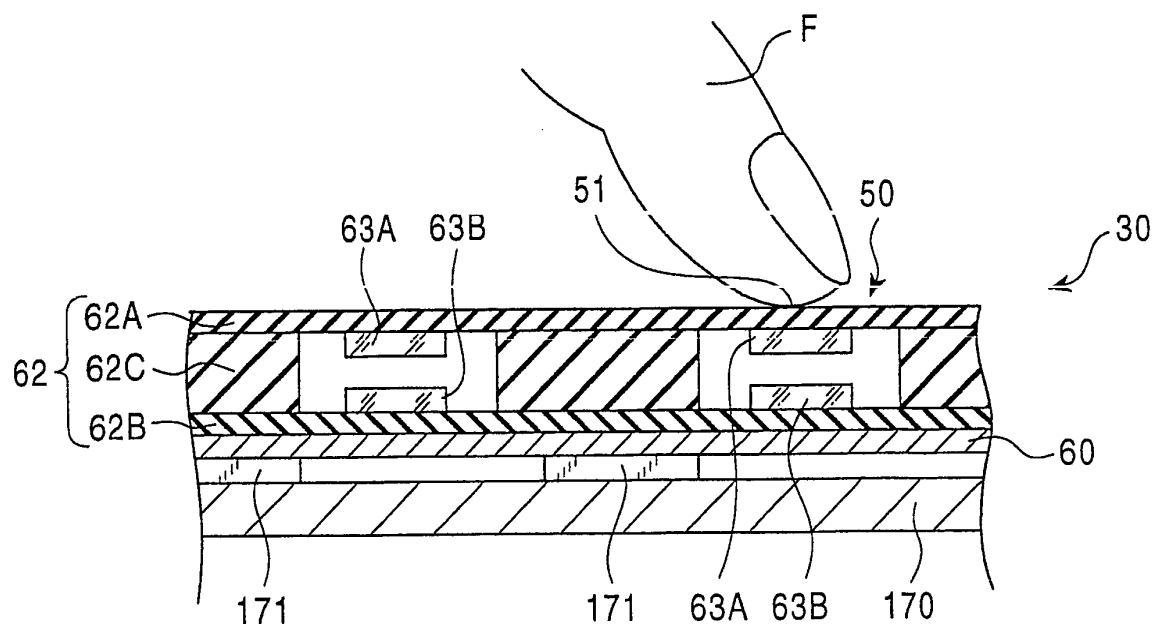


FIG. 11

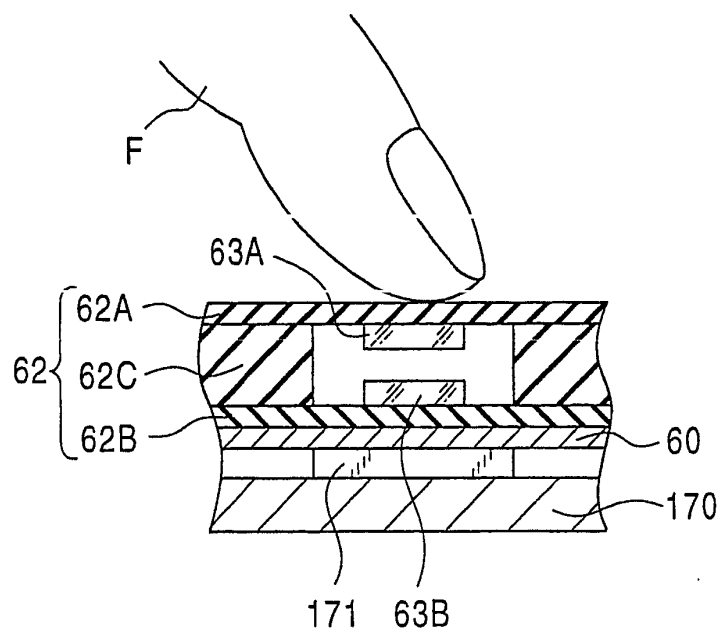


FIG. 12A

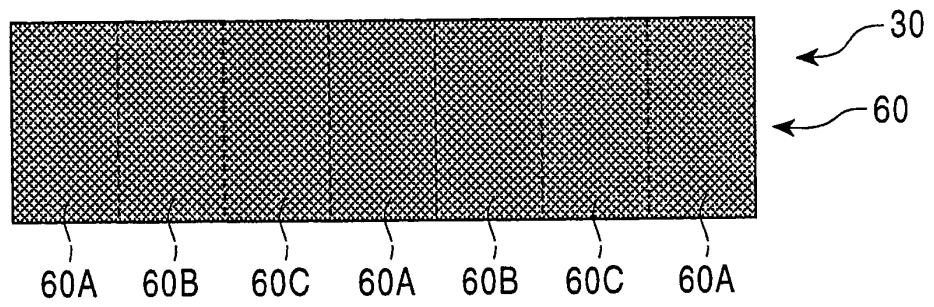


FIG. 12B

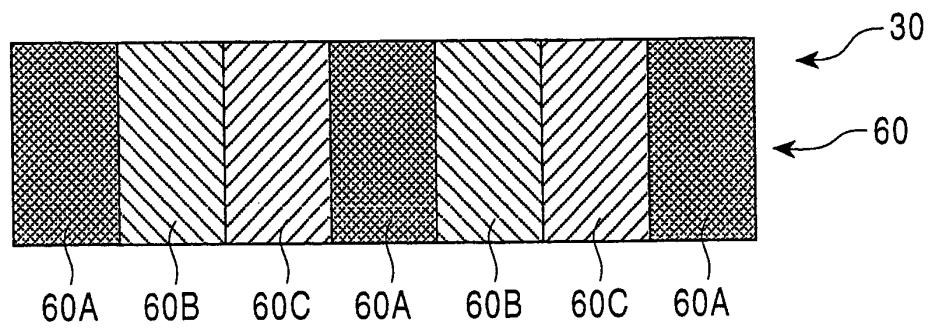


FIG. 13A

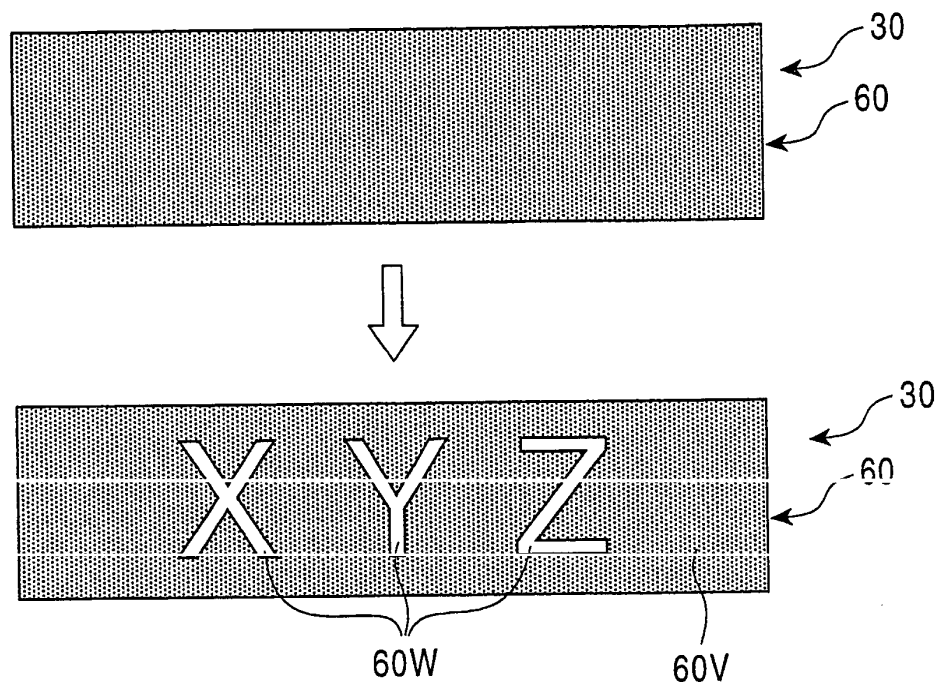


FIG. 13B

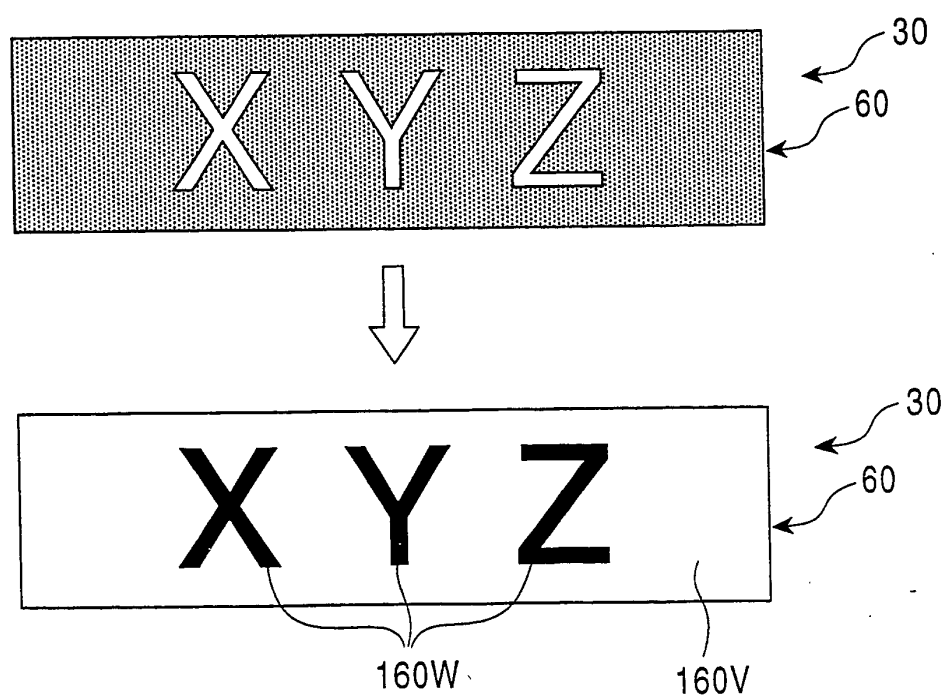


FIG. 14

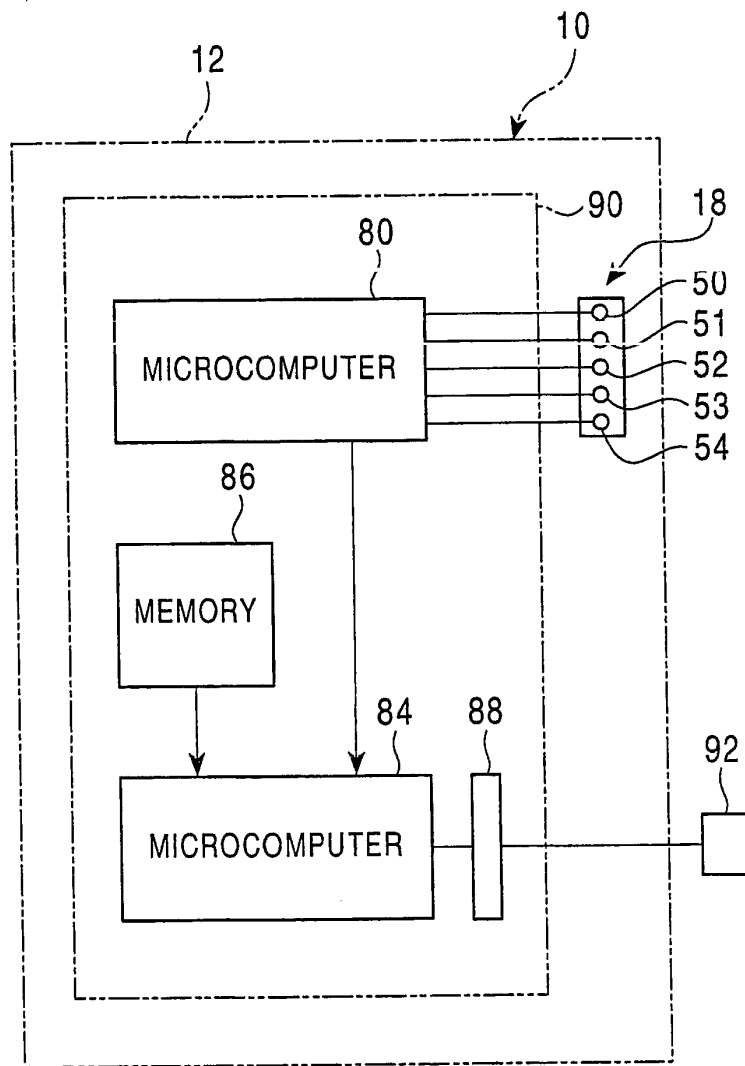


FIG. 15

SAMPLE CASE  
 OUTPUTTING A VOLTAGE  
 CORRESPONDING TO AN INPUT KEY  
 CODE DECIDED BY INTERNAL  
 PROCESSING OF A MICROCOMPUTER  
 ALSO ALLOWABLE

INPUT KEY CODE	OUTPUT VOLTAGE RATIO
VOL+	0.5
VOL-	0.57
STOP	0.59
PLAY/FF	0.73
REW	0.9

(A)

※ OUTPUT VOLTAGE  
 = OUTPUT VOLTAGE RATIO × V<sub>CC</sub>

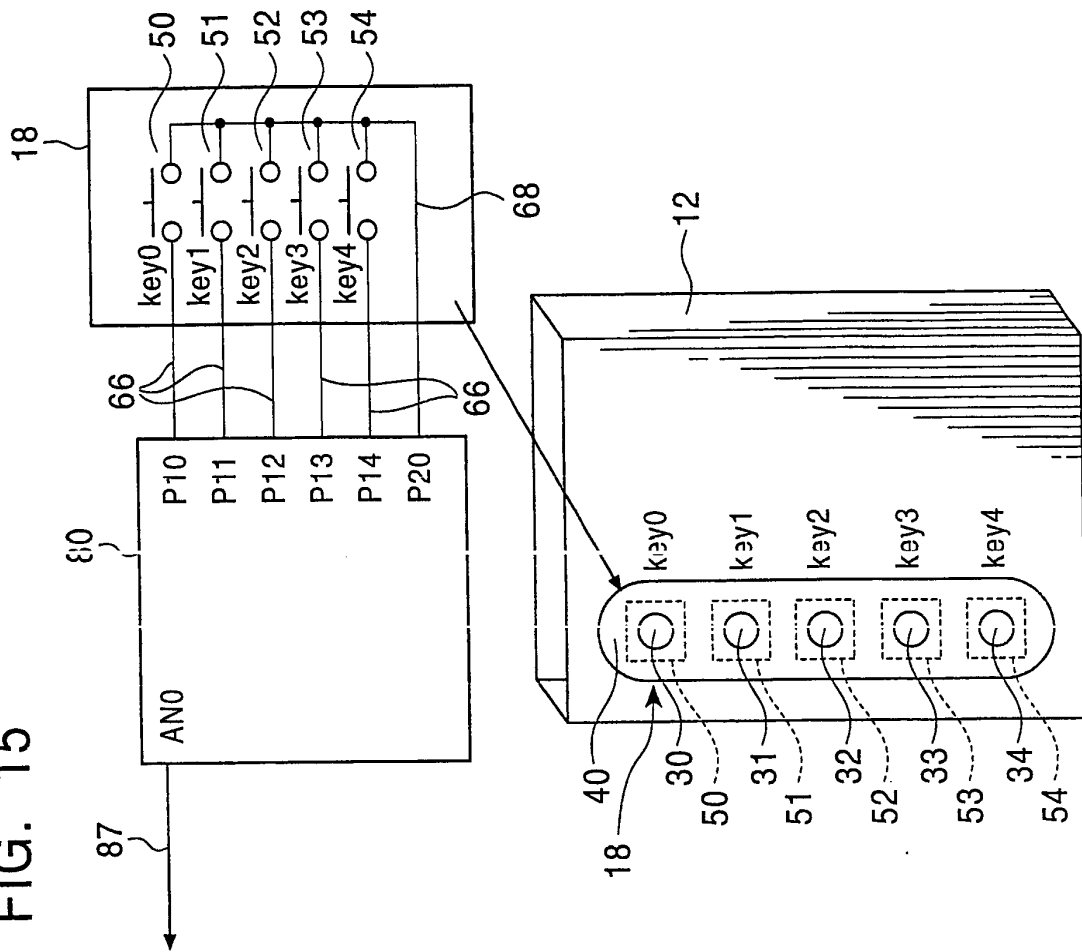


FIG. 16

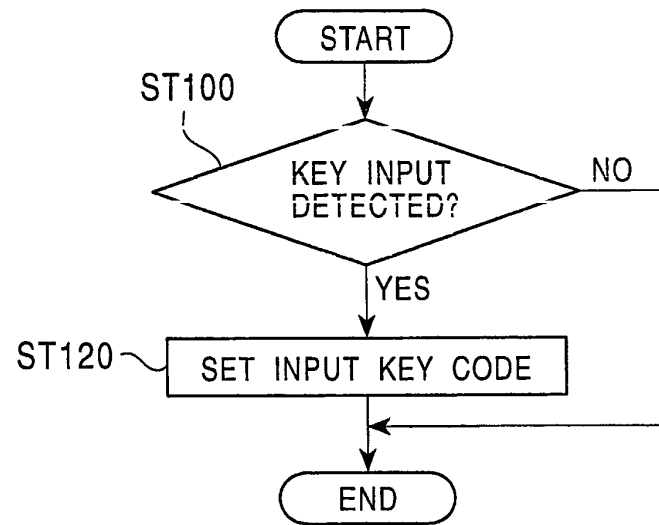




FIG. 17

INPUT KEY CODE DECISION TABLE

	(A) KEY INITIALLY TURNED ON	(B) CURRENT ON KEY	(C) INPUT KEY CODE
(D)	key0	key0	VOL+
	key1	key1	INVALID
	key2	key2	STOP
	key3	key3	INVALID
	key4	key4	VOL-
(E)	key0	key1	PLAY/FF
	key1	key2	PLAY/FF
	key2	key3	PLAY/FF
	key3	key4	PLAY/FF
(F)	key4	key3	REW
	key3	key2	REW
	key2	key1	REW
	key1	key0	REW

FIG. 18

INPUT KEY CODE DECISION SEQUENCE

(A)	<p><u>KEY SCAN</u></p> <p>READS P10 TO P14 TO WHICH THE KEY SWITCH IS CONNECTED, JUDGES WHICH KEY IS ON, AND SETS THE ON KEY AS AN INITIALLY ON KEY</p>
(B)	<p>switch (KEY SCAN WILL BE RE-STARTED AFTER A PREDETERMINED PERIOD)</p>
(C)	<p>case SAME KEY TURNS ON: SET AN INPUT KEY CODE DEFINED FOR THAT KEY</p>
(D)	<p>case ADJACENT KEY TURNS ON: SET AN INPUT KEY CODE DECIDED BASED ON A COMBINATION OF AN INITIALLY ON KEY AND A CURRENT ON KEY</p>
	<p>default OTHERS: SET A CURRENT ON KEY AS AN INITIALLY ON KEY WHILE NEGLECTING A KEY INITIALLY TURNED ON</p>